ABSTRACT

An electronic device and method are provided to enable simulation of a system while minimizing a requirement to reanalyze or recompile topology information during subsequent simulations of the system. Instructions representative of compiling a topology of the system and at least one relationship among a plurality of parameters of the system may be obtained. The instructions, including reading a data structure containing the plurality of parameters to create an intermediate representation representative of the system and the plurality of parameters may be executed. In some implementations, time and effort required to perform system simulations can be reduced, even when parameters that represent the system are changed during each execution of the simulation.